

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of	:	Customer Number: 20277
	:	
Toshihiko TANAKA	:	Confirmation Number: 3832
	:	
Application No.: 10/551,553	:	Group Art Unit: 2812
	:	
Filed: October 03, 2005	:	Examiner: JELSMA, Jonathan G.
	:	
For: PROCESS FOR FABRICATING SEMICONDUCTOR DEVICE AND METHOD FOR GENERATING MASK PATTERN DATA		

AMENDMENT FILED WITH RCE

Mail Stop RCE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

A Request for Continued Examination and the following amendment and remarks are submitted in response to the Office Action mailed on October 2, 2009, having a three-month shortened statutory period for response set to expire January 2, 2010 (Saturday).

Amendments to the Claims are reflected in the listing of claims beginning on page 2 of this paper. This listing of claims replaces all prior versions, and listings, of claims in the application.

Remarks begin on page 4 of this paper.